Name............................

Final - CS U380 - Dec. 18, 2008

No calculators, PDAs, etc.

1 Representation of numbers

a. Assuming an 8-bit word length, What is the two’s-complement representation of -64(base 10)? Show all your work.

b. What is the 32 bit IEEE floating representation of \(8^{1/4}\text{(base10)}\)?

c. What is the eight-bit ASCII representation of ‘$’?
For the following questions, your MIPS code should follow the callee-save conventions for register usage that we have been using in this course.

2 Pointers; Call and return

Translate to MIPS assembly language: (Note: you do NOT have to implement the functions g and h in part b.)

a.)

```c
int
addVars(int *px, int *py, int *psum) {

    int val = *px + *py;
    *psum = val;
    return val;

}
```

b.)

```c
int
fRec(int x, int y) {
    int temp = g(y);
    return temp + h(x + temp);
}
```

*Hint:* What register will you use for *x*? for *temp*?
Extra page, for your use
3 Pointers and arrays

Translate the following procedure, nextHigherChar, into MIPS assembly language, so that it can be called in any program:

```c
void nextHigherChar(char *s) {

    char ch;

    while((ch = *s) != \0) {
        ch = ch + 1
        *s = ch;

        /* increment s */
        s++;
    } /* end while */
}
```
Extra page, for your use
4 Recursion

Calculate the length of a string. Use a method similar to the one for calculating the length of a list in the Felleisen book.

/* Translated literally- don’t eliminate the recursion. */
int strlen(char *s) {
    if (*s == '\0') {
        return 0;
    } else {
        s = s + 1;
        return 1 + strlen(s);
    }
}

.
Extra page, for your use
5 Arrays and function values

Translate to a function in MIPS assembly language.

```c
int countEqualElements(int a[], int b[], int len) {

    int count = 0;
    int i;

    for(i = 0; i < len; i++) {
        if(a[i] == b[i])
            count++;
    }

    return count;
}
```
6  Representation of instructions

For this question, write any addresses that are part of your answer in hexadecimal.

a.

Write one line of assembly language that will assemble into the following instruction:

<table>
<thead>
<tr>
<th>6 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>6 bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>001000</td>
<td>10000</td>
<td>01000</td>
<td>11111</td>
<td>11111</td>
<td>11111</td>
</tr>
</tbody>
</table>

.

b.

Write one line of assembly language that will assemble into the following instruction:

<table>
<thead>
<tr>
<th>6 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>6 bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>000000</td>
<td>10000</td>
<td>10001</td>
<td>01000</td>
<td>00000</td>
<td>101010</td>
</tr>
</tbody>
</table>

.

c.

Write one line of assembly language that will assemble into the following instruction:

<table>
<thead>
<tr>
<th>6 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>5 bits</th>
<th>6 bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>000010</td>
<td>00000</td>
<td>00000</td>
<td>00000</td>
<td>00000</td>
<td>001001</td>
</tr>
</tbody>
</table>