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For ABA problem, see: https://en.wikipedia.org/wiki/ABA_problem

NOTE: The label “pop2a” in *popStack()* below “causes” the bug to be exposed. Try deleting it. This would have the effect of atomically executing:

```
CAS(HEAD, local_myhead, address[local_myhead].next);
```

because it hides the internal assembly code that first computes the register value, *reg_next*, and then executes *CAS()*.

Note that the field “*onStack*” is present only for debugging, and for assertions. Don’t hesitate to add extra fields and variables to ease the job of model checking.

The bug is the ABA problem. When illustrating this bug, the errors in the “Model Checking Results” tab will have too many frames to easily read. You can then remove many of the unnecessary labels in other routines to easily see the cause of the bug: the ABA problem.

Always include these *PlusCal* modules for basic data types.

EXTENDS *Naturals*, *Sequences*, *TLC*, *FiniteSets*

These constants will have to be assigned a value within the generated module.

CONSTANTS *MAX_ITER*, *MAX_STACK_SIZE*

NUM_THREADS is a PRE-DEFINED constant. It cannot be changed later.

NUM_THREADS \triangleq 2

NULL is a unique value (pre-defined constant)

NULL \triangleq CHOOSE *n* : *n* \notin 1 .. *MAX_STACK_SIZE*

--algorithm *LockFreeStack*{

variables *retVal* = [*thread* \in 1 .. *NUM_THREADS* \mapsto *NULL*],
address = [*addr* \in 1 .. *MAX_STACK_SIZE* \mapsto
[*next* \mapsto *NULL*, *onStack* \mapsto FALSE, *data* \mapsto 0]],
initialized = FALSE;
HEAD;

CAS: compare-and-swap

CAS must be a macro. This reflects that it is an assembly instruction

that modifies its arguments. It must not use the call-by-value of a procedure.

CAS is atomic. So, there are no intermediate labels.

macro *CAS*(*x*, *y*, *z*)

```
{
  if ( x = y ) {
    x := z; swap y for z as value of x
    retVal[self] := TRUE;
  } else {
    retVal[self] := FALSE;
```

```

    } ;
  }

procedure pushStack( elt )
  variable local_myhead ;
{
  push1: address[elt].next := HEAD ;
  push2: assert address[elt].onStack = FALSE ;
  push3: address[elt].onStack := TRUE ;
  tryAgainPush: local_myhead := HEAD ;
    push5a: address[elt].next := local_myhead ;
    push5b: CAS(HEAD, local_myhead, elt) ;
    push5c: if (  $\neg$ retVal[self] ) {
    push5d: goto tryAgainPush ;
    } ;
  endPush: return ;
}

procedure popStack( )
  variable local_myhead, reg_next, elt ;
{
  tryAgainPop:
    local_myhead := HEAD ;
  pop1: if ( local_myhead = 0 ) { // If I believe HEAD = 0, return now.
  pop1a: retVal[self] := NULL ;
  pop1b: return ;
    } ;

  pop2: reg_next := address[local_myhead].next ;
  pop2a: CAS(HEAD, local_myhead, reg_next) ;
  pop2b: if (  $\neg$ retVal[self] ) {
  pop2c: goto tryAgainPop ;
    } ;

  pop3: elt := local_myhead ;
  pop4: assert address[elt].onStack = TRUE ;
  pop5: address[elt].onStack := FALSE ;
  pop6: retVal[self] := elt ;
  endPop: return ;
}

process ( thread  $\in$  1 .. NUM_THREADS )
  variable my_set = {}, myelt,
    init_thread, iterations = MAX_ITER ;
{
  init1: init_thread := CHOOSE thr  $\in$  1 .. NUM_THREADS : TRUE ;
  init2: if ( self = init_thread ) { // This thread will initialize global data

```

```

init4: HEAD := 0;
init5: while ( HEAD < MAX_STACK_SIZE ) {
init6:   address[HEAD + 1] := [next ↦ HEAD, onStack ↦ TRUE, data ↦ 1];
init7:   HEAD := HEAD + 1;
        };
init8:   initialized := TRUE; //init_thread will set this global var
        };
init9: await initialized; // all threads will wait for this

th1: while ( iterations > 0 ) {
th2:   either { if ( my_set ≠ {} ) {
        // Set myelt at random (non-deterministically)
th2a:     with ( tmp ∈ my_set ) { myelt := tmp } ;
th2b:     my_set := my_set \ {myelt};
th2c:     call pushStack(myelt);
        } }
        or {
th3a:     call popStack();
th3b:     if ( retVal[self] ≠ NULL ) {
th3c:     my_set := my_set ∪ {retVal[self]} // Add the popped elt to my_set
        } } ;
th4: iterations := iterations - 1;
print iterations ;
} ; end while
} ; end process
} \* end algorithm

```

BEGIN TRANSLATION

Procedure variable *local_myhead* of procedure *pushStack* at line 62 col 12 changed to *local_myhead_*

Procedure variable *elt* of procedure *popStack* at line 77 col 36 changed to *elt_*

CONSTANT *defaultInitValue*

VARIABLES *retVal*, *address*, *initialized*, *HEAD*, *pc*, *stack*, *elt*, *local_myhead_*,
local_myhead, *reg_next*, *elt_*, *my_set*, *myelt*, *init_thread*,
iterations

vars \triangleq \langle *retVal*, *address*, *initialized*, *HEAD*, *pc*, *stack*, *elt*, *local_myhead_*,
local_myhead, *reg_next*, *elt_*, *my_set*, *myelt*, *init_thread*,
iterations \rangle

ProcSet \triangleq (1 .. NUM_THREADS)

Init \triangleq Global variables

\wedge *retVal* = [thread ∈ 1 .. NUM_THREADS ↦ NULL]

\wedge *address* = [addr ∈ 1 .. MAX_STACK_SIZE ↦

[next ↦ NULL, onStack ↦ FALSE, data ↦ 0]]

\wedge *initialized* = FALSE

$$\begin{aligned}
& \wedge \text{HEAD} = \text{defaultInitValue} \\
& \text{Procedure } \text{pushStack} \\
& \wedge \text{elt} = [\text{self} \in \text{ProcSet} \mapsto \text{defaultInitValue}] \\
& \wedge \text{local_myhead_} = [\text{self} \in \text{ProcSet} \mapsto \text{defaultInitValue}] \\
& \text{Procedure } \text{popStack} \\
& \wedge \text{local_myhead} = [\text{self} \in \text{ProcSet} \mapsto \text{defaultInitValue}] \\
& \wedge \text{reg_next} = [\text{self} \in \text{ProcSet} \mapsto \text{defaultInitValue}] \\
& \wedge \text{elt_} = [\text{self} \in \text{ProcSet} \mapsto \text{defaultInitValue}] \\
& \text{Process thread} \\
& \wedge \text{my_set} = [\text{self} \in 1 \dots \text{NUM_THREADS} \mapsto \{\}] \\
& \wedge \text{myelt} = [\text{self} \in 1 \dots \text{NUM_THREADS} \mapsto \text{defaultInitValue}] \\
& \wedge \text{init_thread} = [\text{self} \in 1 \dots \text{NUM_THREADS} \mapsto \text{defaultInitValue}] \\
& \wedge \text{iterations} = [\text{self} \in 1 \dots \text{NUM_THREADS} \mapsto \text{MAX_ITER}] \\
& \wedge \text{stack} = [\text{self} \in \text{ProcSet} \mapsto \langle \rangle] \\
& \wedge \text{pc} = [\text{self} \in \text{ProcSet} \mapsto \text{"init1"}] \\
\\
\text{push1}(\text{self}) & \triangleq \wedge \text{pc}[\text{self}] = \text{"push1"} \\
& \wedge \text{address}' = [\text{address} \text{ EXCEPT } ![\text{elt}[\text{self}]].\text{next} = \text{HEAD}] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{"push2"}] \\
& \wedge \text{UNCHANGED} \langle \text{retVal}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
& \quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
& \quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\\
\text{push2}(\text{self}) & \triangleq \wedge \text{pc}[\text{self}] = \text{"push2"} \\
& \wedge \text{Assert}(\text{address}[\text{elt}[\text{self}]].\text{onStack} = \text{FALSE}, \\
& \quad \text{"Failure of assertion at line 65, column 10."}) \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{"push3"}] \\
& \wedge \text{UNCHANGED} \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
& \quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
& \quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\\
\text{push3}(\text{self}) & \triangleq \wedge \text{pc}[\text{self}] = \text{"push3"} \\
& \wedge \text{address}' = [\text{address} \text{ EXCEPT } ![\text{elt}[\text{self}]].\text{onStack} = \text{TRUE}] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{"tryAgainPush"}] \\
& \wedge \text{UNCHANGED} \langle \text{retVal}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
& \quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
& \quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\\
\text{tryAgainPush}(\text{self}) & \triangleq \wedge \text{pc}[\text{self}] = \text{"tryAgainPush"} \\
& \wedge \text{local_myhead_}' = [\text{local_myhead_} \text{ EXCEPT } ![\text{self}] = \text{HEAD}] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{"push5a"}] \\
& \wedge \text{UNCHANGED} \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \\
& \quad \text{stack}, \text{elt}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
& \quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\\
\text{push5a}(\text{self}) & \triangleq \wedge \text{pc}[\text{self}] = \text{"push5a"}
\end{aligned}$$

$$\begin{aligned}
& \wedge address' = [address \text{ EXCEPT } ![elt[self]].next = local_myhead_ [self]] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"push5b"}] \\
& \wedge \text{UNCHANGED } \langle retVal, initialized, HEAD, stack, elt, \\
& \quad local_myhead_, local_myhead, reg_next, elt_, \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
push5b(self) & \triangleq \wedge pc[self] = \text{"push5b"} \\
& \wedge \text{IF } HEAD = local_myhead_ [self] \\
& \quad \text{THEN } \wedge HEAD' = elt[self] \\
& \quad \wedge retVal' = [retVal \text{ EXCEPT } ![self] = \text{TRUE}] \\
& \quad \text{ELSE } \wedge retVal' = [retVal \text{ EXCEPT } ![self] = \text{FALSE}] \\
& \quad \wedge HEAD' = HEAD \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"push5c"}] \\
& \wedge \text{UNCHANGED } \langle address, initialized, stack, elt, \\
& \quad local_myhead_, local_myhead, reg_next, elt_, \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
push5c(self) & \triangleq \wedge pc[self] = \text{"push5c"} \\
& \wedge \text{IF } \neg retVal[self] \\
& \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"push5d"}] \\
& \quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"endPush"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_, local_myhead, reg_next, elt_, \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
push5d(self) & \triangleq \wedge pc[self] = \text{"push5d"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"tryAgainPush"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_, local_myhead, reg_next, elt_, \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
endPush(self) & \triangleq \wedge pc[self] = \text{"endPush"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = Head(stack[self]).pc] \\
& \wedge local_myhead_ ' = [local_myhead_ \text{ EXCEPT } ![self] = Head(stack[self]).local_myhead_] \\
& \wedge elt' = [elt \text{ EXCEPT } ![self] = Head(stack[self]).elt] \\
& \wedge stack' = [stack \text{ EXCEPT } ![self] = Tail(stack[self])] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, \\
& \quad local_myhead, reg_next, elt_, my_set, myelt, \\
& \quad init_thread, iterations \rangle \\
pushStack(self) & \triangleq push1(self) \vee push2(self) \vee push3(self) \\
& \quad \vee tryAgainPush(self) \vee push5a(self) \vee push5b(self) \\
& \quad \vee push5c(self) \vee push5d(self) \vee endPush(self) \\
tryAgainPop(self) & \triangleq \wedge pc[self] = \text{"tryAgainPop"} \\
& \wedge local_myhead' = [local_myhead \text{ EXCEPT } ![self] = HEAD] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"pop1"}]
\end{aligned}$$

$$\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \text{local_myhead_}, \text{reg_next}, \text{elt_}, \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle$$

$$\text{pop1}(\text{self}) \triangleq \wedge \text{pc}[\text{self}] = \text{"pop1"}$$

$$\wedge \text{IF } \text{local_myhead}[\text{self}] = 0$$

$$\quad \text{THEN } \wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop1a"}]$$

$$\quad \text{ELSE } \wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop2"}]$$

$$\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle$$

$$\text{pop1a}(\text{self}) \triangleq \wedge \text{pc}[\text{self}] = \text{"pop1a"}$$

$$\wedge \text{retVal}' = [\text{retVal EXCEPT } ![\text{self}] = \text{NULL}]$$

$$\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop1b"}]$$

$$\wedge \text{UNCHANGED } \langle \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle$$

$$\text{pop1b}(\text{self}) \triangleq \wedge \text{pc}[\text{self}] = \text{"pop1b"}$$

$$\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{Head}(\text{stack}[\text{self}]).\text{pc}]$$

$$\wedge \text{local_myhead}' = [\text{local_myhead EXCEPT } ![\text{self}] = \text{Head}(\text{stack}[\text{self}]).\text{local_myhead}]$$

$$\wedge \text{reg_next}' = [\text{reg_next EXCEPT } ![\text{self}] = \text{Head}(\text{stack}[\text{self}]).\text{reg_next}]$$

$$\wedge \text{elt_}' = [\text{elt_ EXCEPT } ![\text{self}] = \text{Head}(\text{stack}[\text{self}]).\text{elt_}]$$

$$\wedge \text{stack}' = [\text{stack EXCEPT } ![\text{self}] = \text{Tail}(\text{stack}[\text{self}])]$$

$$\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{elt}, \text{local_myhead_}, \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle$$

$$\text{pop2}(\text{self}) \triangleq \wedge \text{pc}[\text{self}] = \text{"pop2"}$$

$$\wedge \text{reg_next}' = [\text{reg_next EXCEPT } ![\text{self}] = \text{address}[\text{local_myhead}[\text{self}]].\text{next}]$$

$$\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop2a"}]$$

$$\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \text{local_myhead_}, \text{local_myhead}, \text{elt_}, \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle$$

$$\text{pop2a}(\text{self}) \triangleq \wedge \text{pc}[\text{self}] = \text{"pop2a"}$$

$$\wedge \text{IF } \text{HEAD} = \text{local_myhead}[\text{self}]$$

$$\quad \text{THEN } \wedge \text{HEAD}' = \text{reg_next}[\text{self}]$$

$$\quad \wedge \text{retVal}' = [\text{retVal EXCEPT } ![\text{self}] = \text{TRUE}]$$

$$\quad \text{ELSE } \wedge \text{retVal}' = [\text{retVal EXCEPT } ![\text{self}] = \text{FALSE}]$$

$$\quad \wedge \text{HEAD}' = \text{HEAD}$$

$$\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop2b"}]$$

$$\wedge \text{UNCHANGED } \langle \text{address}, \text{initialized}, \text{stack}, \text{elt}, \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle$$

$$\begin{aligned}
\text{pop2b}(\text{self}) &\triangleq \wedge \text{pc}[\text{self}] = \text{"pop2b"} \\
&\wedge \text{IF } \neg \text{retVal}[\text{self}] \\
&\quad \text{THEN } \wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop2c"}] \\
&\quad \text{ELSE } \wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop3"}] \\
&\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
&\quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
&\quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\text{pop2c}(\text{self}) &\triangleq \wedge \text{pc}[\text{self}] = \text{"pop2c"} \\
&\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"tryAgainPop"}] \\
&\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
&\quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
&\quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\text{pop3}(\text{self}) &\triangleq \wedge \text{pc}[\text{self}] = \text{"pop3"} \\
&\wedge \text{elt_}' = [\text{elt_ EXCEPT } ![\text{self}] = \text{local_myhead}[\text{self}]] \\
&\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop4"}] \\
&\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
&\quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{my_set}, \\
&\quad \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\text{pop4}(\text{self}) &\triangleq \wedge \text{pc}[\text{self}] = \text{"pop4"} \\
&\wedge \text{Assert}(\text{address}[\text{elt_}[\text{self}]].\text{onStack} = \text{TRUE}, \\
&\quad \text{"Failure of assertion at line 92, column 9."}) \\
&\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop5"}] \\
&\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
&\quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
&\quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\text{pop5}(\text{self}) &\triangleq \wedge \text{pc}[\text{self}] = \text{"pop5"} \\
&\wedge \text{address}' = [\text{address EXCEPT } ![\text{elt_}[\text{self}]].\text{onStack} = \text{FALSE}] \\
&\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"pop6"}] \\
&\wedge \text{UNCHANGED } \langle \text{retVal}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
&\quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
&\quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\text{pop6}(\text{self}) &\triangleq \wedge \text{pc}[\text{self}] = \text{"pop6"} \\
&\wedge \text{retVal}' = [\text{retVal EXCEPT } ![\text{self}] = \text{elt_}[\text{self}]] \\
&\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{"endPop"}] \\
&\wedge \text{UNCHANGED } \langle \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\
&\quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\
&\quad \text{my_set}, \text{myelt}, \text{init_thread}, \text{iterations} \rangle \\
\text{endPop}(\text{self}) &\triangleq \wedge \text{pc}[\text{self}] = \text{"endPop"} \\
&\wedge \text{pc}' = [\text{pc EXCEPT } ![\text{self}] = \text{Head}(\text{stack}[\text{self}]).\text{pc}] \\
&\wedge \text{local_myhead}' = [\text{local_myhead EXCEPT } ![\text{self}] = \text{Head}(\text{stack}[\text{self}]).\text{local_myhead}] \\
&\wedge \text{reg_next}' = [\text{reg_next EXCEPT } ![\text{self}] = \text{Head}(\text{stack}[\text{self}]).\text{reg_next}]
\end{aligned}$$

$$\begin{aligned}
& \wedge elt_ = [elt_ \text{ EXCEPT } ![self] = Head(stack[self]).elt_] \\
& \wedge stack' = [stack \text{ EXCEPT } ![self] = Tail(stack[self])] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, elt, \\
& \quad local_myhead_ , my_set, myelt, init_thread, \\
& \quad iterations \rangle \\
popStack(self) & \triangleq tryAgainPop(self) \vee pop1(self) \vee pop1a(self) \\
& \quad \vee pop1b(self) \quad \vee pop2(self) \vee pop2a(self) \\
& \quad \vee pop2b(self) \quad \vee pop2c(self) \vee pop3(self) \\
& \quad \vee pop4(self) \vee pop5(self) \vee pop6(self) \\
& \quad \vee endPop(self) \\
init1(self) & \triangleq \wedge pc[self] = \text{"init1"} \\
& \wedge init_thread' = [init_thread \text{ EXCEPT } ![self] = \text{CHOOSE } thr \in 1 \dots NUM_THREADS : \text{TRUE}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init2"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, iterations \rangle \\
init2(self) & \triangleq \wedge pc[self] = \text{"init2"} \\
& \wedge \text{IF } self = init_thread[self] \\
& \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init4"}] \\
& \quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init9"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
init4(self) & \triangleq \wedge pc[self] = \text{"init4"} \\
& \wedge HEAD' = 0 \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init5"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
init5(self) & \triangleq \wedge pc[self] = \text{"init5"} \\
& \wedge \text{IF } HEAD < MAX_STACK_SIZE \\
& \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init6"}] \\
& \quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init8"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
init6(self) & \triangleq \wedge pc[self] = \text{"init6"} \\
& \wedge address' = [address \text{ EXCEPT } ![HEAD + 1] = [next \mapsto HEAD, onStack \mapsto \text{TRUE}, data \mapsto 1]] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init7"}] \\
& \wedge \text{UNCHANGED } \langle retVal, initialized, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ ,
\end{aligned}$$

$$\begin{aligned}
& \text{my_set, myelt, init_thread, iterations} \rangle \\
\text{init7}(self) & \triangleq \wedge pc[self] = \text{"init7"} \\
& \wedge HEAD' = HEAD + 1 \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init5"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
\text{init8}(self) & \triangleq \wedge pc[self] = \text{"init8"} \\
& \wedge initialized' = \text{TRUE} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"init9"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
\text{init9}(self) & \triangleq \wedge pc[self] = \text{"init9"} \\
& \wedge initialized \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th1"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
\text{th1}(self) & \triangleq \wedge pc[self] = \text{"th1"} \\
& \wedge \text{IF } iterations[self] > 0 \\
& \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th2"}] \\
& \quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"Done"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
\text{th2}(self) & \triangleq \wedge pc[self] = \text{"th2"} \\
& \wedge \vee \wedge \text{IF } my_set[self] \neq \{\} \\
& \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th2a"}] \\
& \quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th4"}] \\
& \quad \vee \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th3a"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, myelt, init_thread, iterations \rangle \\
\text{th2a}(self) & \triangleq \wedge pc[self] = \text{"th2a"} \\
& \wedge \exists tmp \in my_set[self] : \\
& \quad myelt' = [myelt \text{ EXCEPT } ![self] = tmp] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th2b"}] \\
& \wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
& \quad local_myhead_ , local_myhead, reg_next, elt_ , \\
& \quad my_set, init_thread, iterations \rangle
\end{aligned}$$

$$\begin{aligned}
th2b(self) &\triangleq \wedge pc[self] = \text{"th2b"} \\
&\wedge my_set' = [my_set \text{ EXCEPT } ![self] = my_set[self] \setminus \{myelt[self]\}] \\
&\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th2c"}] \\
&\wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
&\quad local_myhead_ , local_myhead, reg_next, elt_ , \\
&\quad myelt, init_thread, iterations \rangle \\
th2c(self) &\triangleq \wedge pc[self] = \text{"th2c"} \\
&\wedge \wedge elt' = [elt \text{ EXCEPT } ![self] = myelt[self]] \\
&\quad \wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"pushStack"}, \\
&\quad pc \mapsto \text{"th4"}, \\
&\quad local_myhead_ \mapsto local_myhead_ [self], \\
&\quad elt \mapsto elt[self]] \\
&\quad \circ stack[self]] \\
&\wedge local_myhead_ ' = [local_myhead_ \text{ EXCEPT } ![self] = defaultInitValue] \\
&\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"push1"}] \\
&\wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, local_myhead, \\
&\quad reg_next, elt_ , my_set, myelt, init_thread, \\
&\quad iterations \rangle \\
th3a(self) &\triangleq \wedge pc[self] = \text{"th3a"} \\
&\wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"popStack"}, \\
&\quad pc \mapsto \text{"th3b"}, \\
&\quad local_myhead \mapsto local_myhead[self], \\
&\quad reg_next \mapsto reg_next[self], \\
&\quad elt_ \mapsto elt_ [self]] \\
&\quad \circ stack[self]] \\
&\wedge local_myhead' = [local_myhead \text{ EXCEPT } ![self] = defaultInitValue] \\
&\wedge reg_next' = [reg_next \text{ EXCEPT } ![self] = defaultInitValue] \\
&\wedge elt_ ' = [elt_ \text{ EXCEPT } ![self] = defaultInitValue] \\
&\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"tryAgainPop"}] \\
&\wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, elt, \\
&\quad local_myhead_ , my_set, myelt, init_thread, \\
&\quad iterations \rangle \\
th3b(self) &\triangleq \wedge pc[self] = \text{"th3b"} \\
&\wedge \text{IF } retVal[self] \neq NULL \\
&\quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th3c"}] \\
&\quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th4"}] \\
&\wedge \text{UNCHANGED } \langle retVal, address, initialized, HEAD, stack, elt, \\
&\quad local_myhead_ , local_myhead, reg_next, elt_ , \\
&\quad my_set, myelt, init_thread, iterations \rangle \\
th3c(self) &\triangleq \wedge pc[self] = \text{"th3c"} \\
&\wedge my_set' = [my_set \text{ EXCEPT } ![self] = my_set[self] \cup \{retVal[self]\}] \\
&\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"th4"}]
\end{aligned}$$

$$\wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\ \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\ \text{myelt}, \text{init_thread}, \text{iterations} \rangle$$

$$\begin{aligned} \text{th4}(self) \triangleq & \wedge \text{pc}[self] = \text{"th4"} \\ & \wedge \text{iterations}' = [\text{iterations EXCEPT } ![self] = \text{iterations}[self] - 1] \\ & \wedge \text{PrintT}(\text{iterations}'[self]) \\ & \wedge \text{pc}' = [\text{pc EXCEPT } ![self] = \text{"th1"}] \\ & \wedge \text{UNCHANGED } \langle \text{retVal}, \text{address}, \text{initialized}, \text{HEAD}, \text{stack}, \text{elt}, \\ & \quad \text{local_myhead_}, \text{local_myhead}, \text{reg_next}, \text{elt_}, \\ & \quad \text{my_set}, \text{myelt}, \text{init_thread} \rangle \end{aligned}$$

$$\begin{aligned} \text{thread}(self) \triangleq & \text{init1}(self) \vee \text{init2}(self) \vee \text{init4}(self) \vee \text{init5}(self) \\ & \vee \text{init6}(self) \vee \text{init7}(self) \vee \text{init8}(self) \\ & \vee \text{init9}(self) \vee \text{th1}(self) \vee \text{th2}(self) \vee \text{th2a}(self) \\ & \vee \text{th2b}(self) \vee \text{th2c}(self) \vee \text{th3a}(self) \vee \text{th3b}(self) \\ & \vee \text{th3c}(self) \vee \text{th4}(self) \end{aligned}$$

$$\begin{aligned} \text{Next} \triangleq & (\exists self \in \text{ProcSet} : \text{pushStack}(self) \vee \text{popStack}(self)) \\ & \vee (\exists self \in 1 .. \text{NUM_THREADS} : \text{thread}(self)) \\ & \vee \text{Disjunct to prevent deadlock on termination} \\ & ((\forall self \in \text{ProcSet} : \text{pc}[self] = \text{"Done"}) \wedge \text{UNCHANGED } \text{vars}) \end{aligned}$$

$$\text{Spec} \triangleq \text{Init} \wedge \square[\text{Next}]_{\text{vars}}$$

$$\text{Termination} \triangleq \diamond(\forall self \in \text{ProcSet} : \text{pc}[self] = \text{"Done"})$$

END TRANSLATION

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\ * Modification History
\ * Last modified Thu Apr 11 15:49:50 EDT 2019 by gene
\ * Last modified Sun Oct 22 06:26:25 EDT 2017 by celestekostopulos-cooperman
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