

## Day 7

#### Admin:

- hw2 due today @ 11:59 PM
- hw3 available now

#### Content:

- Computer Representation of sets
- Negation (DeMorgan's Laws)
- set algebra & logic algebra (very similar!)
- Logic (digital) circuits

# Computer representation of sets:

How does a computer store the following sets?

$$U = \{10, \\ A = \{10, \\ B = \{10, \\ C = \{10, \\ 128, 8358, 12, 0, -100\} \\ 8358, 0, -100\}$$
 (the universal set, contains all items another set contains all items another set contains the set of the universal set, contains all items another set contains all items and all items and all items and all items and all items are set contains all item

# Approach:

Step 1: Assign a natural number (0, 1, 2, 3...) index (position) to all the items in universal set: Step 2: Represent a set as a bit string (sequence of bits).

If bit0 is 1, item0 in set.

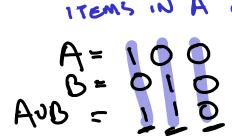
If bit1 is 0, item1 not in set.

# Computer representation of sets: Why is the bit-string a good idea?

1. We need only store every item once, which is important if some of our items would take a lot of memory to store:

```
A = {901824918240192491283938}
B = {901824918240192491283938, 1}
C = {901824918240192491283938, 1, 2}
```

2. Our set operation have a natural correspondance with logical operations:  $A \cup S$ 

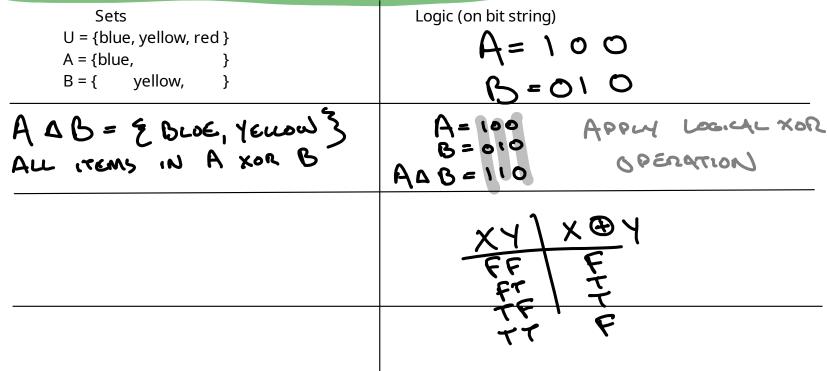


Many logical operations on bit string correspond to a set operation Sets Logic (on bit string) A=100 U = {blue, yellow, red } A = {blue, }  $B = \{ yellow, \}$ B=010 ALL ITEMS NOT IN A EACH BIT NEGATED APPLY LOGICAL OR AUB= & BLOE, YELLOW } ALL TEMS IN A OR B A = 100 B = 010 A & B = 110 MOTARISTO

And= Ø ALL ITEMS IN A AND B

Apply LOGICAL A CHOTARSO

# Many logical operations on bit string correspond to a set operation



$$\neg (X \vee Y) = \neg X \wedge \neg Y$$

(A)B) C ANB ASSUME X E (A)B) A COMPLEMENT X & (A)B) A COMPLEMENT (X&A) AND (X&B) DUNION)

XEAE AND XEB & COMPLEMENT XE ACOBOR INTERSECTION

SO XE (AOD) - XE ACNBC

(AUB) 2 A nos XEA AND XEB COMPLEMENT X&A AND X&B & COMPLEMENT X& (AUB) & UNION Assume XE ACAB XE (AUB) COMPLEMENT

AFTER ALL THAT WORK WE'VE PROVED (ONE OF) DEMORGAN'S LAW FOR SETS

(Aub) = AcnB

FEEL FAMILIAR?

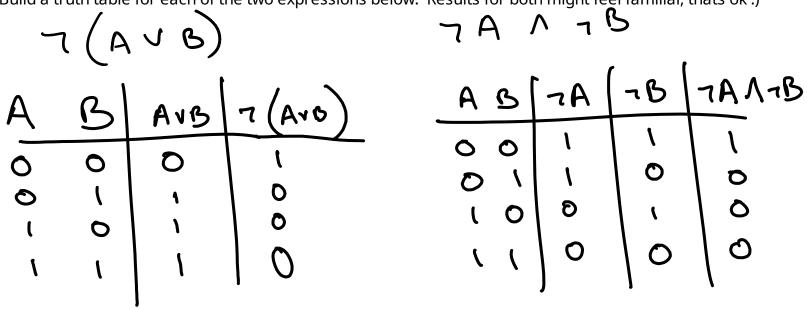
Swapping operators: sets and logic

LOGIC NEGATION COMPLEMENT AND 1 INTERSECTION INCLUSIVE OR V () NION (AVB) = 7A 17B

FEEL FAMILIAR YET?

# In Class Assignment (not for today, this is complete from day 4's notes):

Build a truth table for each of the two expressions below. Results for both might feel familiar, thats ok:)



<take a="" at="" logic_set_identities.pdf="" look="" together=""></take>	
(available on course website next to today's notes)	

#### Absorption Laws

$$P \wedge (P \vee Q) = P$$
  
 $P \vee (P \wedge Q) = P$ 

$$A \cap (A \cup B) = A$$
$$A \cup (A \cap B) = A$$

#### Complement Laws

$$P \lor \neg P = T \cdot P \land \neg P = F$$

$$A \cup A^C = U$$
$$A \cap A^C = \emptyset$$

"operation that when done to item, returns the item" Idempotent Laws ڃ

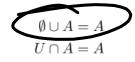
$$P \lor P = P$$

$$P \land P = P$$

$$A \cup A = A$$
  
 $A \cap A = A$ 

#### Identity

False 
$$\vee$$
 P = P  
True  $\wedge$  P = P



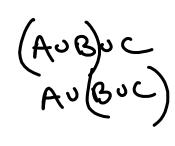
#### Domination:

True 
$$\vee$$
 P = True  
False  $\wedge$  P = False

$$U \cup A = U$$
$$\emptyset \cap A = \emptyset$$

# **Associative Laws**

$$(P \lor Q) \lor R = P \lor (Q \lor R)$$
$$(P \land Q) \land R = P \land (Q \land R)$$



$$(A \cup B) \cup C = A \cup (B \cup C)$$
$$(A \cap B) \cap C = A \cap (B \cap C)$$

# **Double Negation**

$$\neg \neg P = P$$



$$(A^C)^C = A$$

DeMorgan's Laws
$$\neg (P \lor Q) = \neg P \land \neg Q$$

$$\neg(P \land Q) = \neg P \lor \neg Q$$

$$(A \cup B)^C = A^C \cap B^C$$
$$(A \cap B)^C = A^C \cup B^C$$

# Distributive Laws

$$P \wedge (Q \vee R) = (P \wedge Q) \vee (P \wedge R)$$
  
 $P \vee (Q \wedge R) = (P \vee Q) \wedge (P \vee R)$ 



$$A \cap (B \cup C) = (A \cap B) \cup (A \cap C)$$
$$A \cup (B \cap C) = (A \cup B) \cap (A \cup C)$$

Simplifying boolean or set expressions (set / logic algebra)

Simplifying boolean or set expressions (set / logic algebra)

$$(xy) \cap (xy')$$

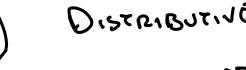
$$= X \cup (Y \cap Y') \quad Distributive$$

$$= X \cup (Y \cap Y') \quad Complement$$

$$= X \cup ($$

$$\begin{array}{cccc} & & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & \\ & & \\$$









BECAUSE SET LOGIC ALGEBRA IS SO

SIMILAR, CAN I MIX SWAP NOTATION?

PLEASE DON'T

IN CLASS ACTION (3\$5).7 t LABEL STEPS SIMPLIFY (AUB)nAc = 3.7 +5.7 = (AnAc) O (BNAc) DISTRIBUT INE v (Bn4) COMPLEMENT 10527174 BNA

YVX

IDENTITY

<lego logic gate video https://youtu.be/RA2po1xk\_0A?t=5 >

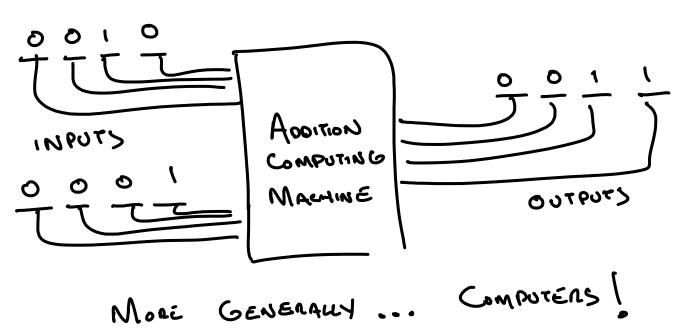
You can build logic gates (AND, OR, NOT) out of real life things!

- legos
- (0 = pin pushed in, 1=pin pulled out)
   electronics
  - (0=low voltage, 1=high voltage)
- (U=low voltage, T=nigh voltage

- water

- (0 = empty tube, 1 = tube has water)
- mechanical switches & gears
- (0 = lever is down, 1 = lever is up)

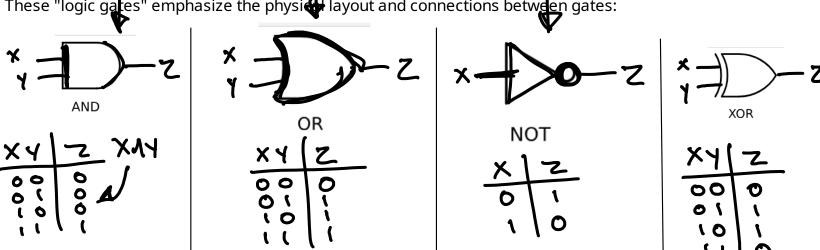
Why would you want to build logic gates out of real-life things?

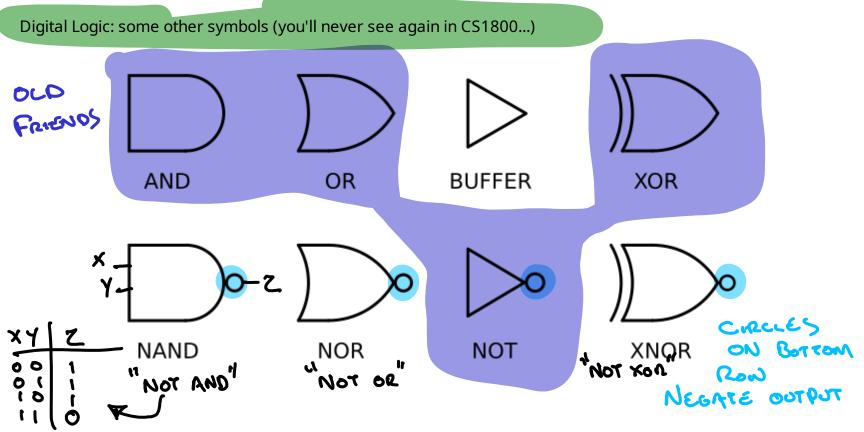


# Digital Logic (another way of expression boolean algebra)

Many of these gates have to consider the physical layout of their inputs (pins, water, cable etc) so they can be arranged to produce intended behavior.

These "logic gates" emphasize the physidal layout and connections between gates:

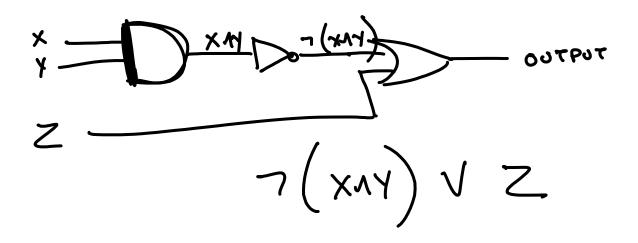




## Digital Logic: circuits

A circuit is a collection of logic gates which have been connected.

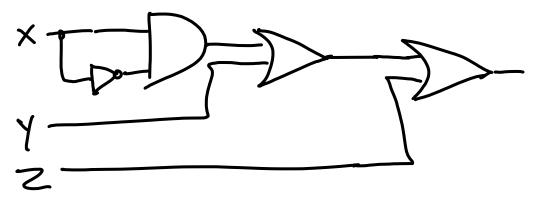
What logic expression is equivilent to the output below?



## In Class Activity

For the circuit shown below:

- express it using logical symbols
- simplify this expression using the logical identities shown earlier (label each step please)
- draw a new circuit which is equivilent to your simplified expression



if time / for fun: design your own super complex circuit which is equivilent to something much simpler (see also, "rube goldberg machine")

